

Applicant : Mitsuaki Osame et al.
Serial No. : 10/724,365
Filed : December 1, 2003
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Attorney's Docket No.: 12732-183001 / US6776

**Amendments to the Specification:**

Please replace the paragraph beginning at page 1, line 1 through the end of the application, including the abstract with the attached substitute specification in English.

Please also enter the following amendments:

Please replace the paragraph beginning at page 8, line 30 with the following amended paragraph:

FIGS. 1(A) and 1(B) are diagrams showing an embodiment mode of the invention.

Please replace the paragraph beginning at page 9, line 1 with the following amended paragraph:

FIGS. 2(A) and 2(B) are diagrams showing a conventional data latch circuit.

Please replace the paragraph beginning at page 9, line 2 with the following amended paragraph:

FIGS. 3(A) and 3(B) are diagrams showing the V_{IN} - V_{OUT} characteristics of a general inverter.

Please replace the paragraph beginning at page 9, line 5 with the following amended paragraph:

FIGS. 6(A) to 6(C) are diagrams showing an embodiment mode of the invention.

Please replace the paragraph beginning at page 9, line 7 with the following amended paragraph:

FIGS. 8(A) and 8(B) are diagrams showing an embodiment mode of the invention.

Please replace the paragraph beginning at page 9, line 8 with the following amended paragraph:

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FIGS. 9(A) to 9(D) are views of electronic devices to which the invention can be applied.

Please replace the paragraph beginning at page 9, line 9 with the following amended paragraph:

FIGS. 10(A) and 10(B) are diagrams showing a general clocked inverter.